

APPLICATION  
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TITLE: MAGNETIC TRACK FOLLOWING SERVO ALGORITHM  
USING SIGNAL QUALITY

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5                   MAGNETIC TRACK FOLLOWING SERVO ALGORITHM USING  
                    SIGNAL QUALITY

BACKGROUND OF THE INVENTION

10               The invention relates generally to head positioning  
servo systems for reducing misalignment between heads and data  
tracks in data storage systems.

15               In multi-track magnetic tape storage systems, random  
lateral tape motion ("LTM") is a limiting factor in achieving  
higher track densities and higher user data capacity per tape.  
LTM is the random motion of a tape in a direction lateral to the  
direction of tape transport. LTM is impacted by variations in  
tension, number of tape guides used, as well as other factors.  
These factors affect LTM in various ways. Some may cause abrupt  
20 momentary jumps while others may cause a gradual shift over time.  
During a read, LTM causes mis-registration of the read head over  
the track being read. Such mis-registration results in read data  
error.

25               Various servo techniques or approaches have been  
developed to reduce the effects of tracking error caused by LTM  
and thus improve track density. Although known servo techniques  
vary somewhat, most involve mechanisms for dynamically moving the  
read head laterally to continually re-position it over the  
recorded data track. They also use servo heads to provide  
30 corrective positioning information. For example, one approach  
reads recorded servo information, either dedicated (that is,  
located on a continuous track of servo information) or embedded  
within the user data, to produce a position error signal for  
accurate head positioning. Yet another approach uses two  
35 auxiliary servo heads to follow each edge of an adjacent data

track to produce a position error signal. In both of these approaches, the position error signal is based on signal amplitude.

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## SUMMARY OF THE INVENTION

In one aspect, the invention provides a method and apparatus for controlling a data head for reading data from a data track on a magnetic tape in a magnetic tape drive. They  
10 include determining signal quality for read data signals produced by a data head reading data from a data track and adjusting the position of the data head relative to the data track using the signal quality.

Embodiments of the invention may include one or more of  
15 the following features.

Adjusting includes performing a seek operation that includes changing the position of the data head and determining changes in the signal quality corresponding to the changes in data head position until a predetermined level of improvement in  
20 the signal quality is achieved.

Changing the position of the data head includes stepping the data head laterally across the data track. Performing the seek operation further includes using the determined changes to determine direction and size of steps of  
25 the stepping. Performing the seek operation further includes comparing each of the determined changes to a lower threshold and a current number of steps taken by seek operation to a maximum number of steps.

Adjusting further includes maintaining the data head at  
30 a current position without stepping when results of the comparison indicate that the determined change is below the lower threshold and the current number of steps taken exceeds the

maximum number of steps.

Adjusting further includes monitoring the signal quality while maintaining the data head at the current position to detect any changes in the signal quality greater than the lower threshold and, if any changes greater than the lower threshold are detected, repeating the performance of the seek operation. Alternatively, adjusting can further include determining that the data head has been maintained at the current position without stepping for a period of time in excess of a predetermined maximum re-seek time threshold and repeating the performance of the seek operation.

In another aspect, the invention provides a tape drive system. The tape drive system includes a data head structure to produce read data signals from data recorded on a data track of a tape, a head stepper coupled to the data head structure and a data channel unit to produce read data signal quality values from the read data signals. The tape drive system further includes a servo controller coupled to the head stepper and the data channel unit, the servo control being configured to use the signal quality values to control adjustment of the position of the data head structure relative to the data track by the head stepper.

Particular implementations of the invention may provide one or more of the following advantages. The need for recorded servo information and/or servo heads for positioning read heads are eliminated. Also, because read data signal quality values (i.e., error signals) are a finer determiner of center of track than read data signal amplitude, bit error rates are reduced. In addition, the search for the lowest level of error (or noise) enables track following through shifts in track location in a manner that, unlike prior head positioning techniques, avoids placing a head at or near the edge of a data track where it can be knocked off of the track by even minor disturbances.

Other features and advantages of the invention will be apparent from the following detailed description and from the claims.

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#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram of a tape drive system employing a read servo process that uses read data signal quality measurements to adjust alignment of a read data head with respect to a track from which data is being read.

FIG. 2 is a block diagram of a PRML data channel unit that performs the read data signal quality measurements for the tape drive system of FIG. 1.

FIGS. 3A and 3B are flow diagrams of the read servo process, which includes four states: an initial/no step state; an initial step state; a stepping state and a streaming (no step) state.

FIG. 4 is a flow diagram of the initial/no step state.

FIG. 5 is a flow diagram of the initial step state.

FIG. 6 is a flow diagram of the stepping state.

FIG. 7 is a flow diagram of the streaming (no step) state.

#### DETAILED DESCRIPTION

Referring to FIG. 1, an exemplary magnetic tape drive system 10 is shown. The tape drive system 10 includes a tape transport system schematically represented by two reels 12, a supply reel 12a and a take-up reel 12b, mounted on rotatably driven tape transport motors 14a, 14b, respectively. The tape transport motors 14 rotate the reels 12 to transport a tape 16 from one reel to the other. The tape 16 has one or more longitudinal tracks of data recorded thereon. As the tape



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Read preamplifiers of an analog preamplifier and write driver block 28 amplify the read signals from the data head 18. The amplified read signals are fed to a data channel unit 30, which supplies the data read from the tape 16 (via the block 28) to the data controller 26 and provides write data from the data controller 26 to the block 28. The servo controller 20 may be used to control the functions of the data channel unit 30, such as filter settings, type of data encoding and decoding, write current level, and amount of write precompensation.

The data channel unit 30 is a Partial-Response signaling with Maximum-Likelihood sequence detection (PRML) data channel. The PRML techniques performed by the data channel are well known in the art. They are used to maximize the likelihood that the data reconstructed by a data recovery system such as the tape drive system 10 is accurate. Thus, the data channel unit 30 uses partial-response signaling to produce signals with "controlled" intersymbol interference during a write operation and maximum-likelihood sequence to produce the most likely estimation of the recorded data during a read operation.

During a read operation, the data controller 26 performs the error correcting functions and supplies the read data as processed by the data channel unit 30 to the host interface 24. Conversely, during write operations, the data controller 26 encodes and formats write data received from the host interface 24 and supplies it to the channel unit 30. A write driver of the block 28 drives the data head structure 18, causing the write data as processed by the data channel unit 30 to be recorded on the tape 16.

The data controller 26 sends track position commands to the servo controller 20, which issues commands to a head stepper 32 for data head positioning. The head stepper 32 (which includes both stepper motor and driver functionality) operates to

move the data head structure 18 laterally across the tape 16 to switch tracks. In addition to supporting track selection, the servo controller 20 is able to provide instructions to the head stepper 32 for adjusting the data head structure 18 with respect to a selected track so as to center the head over that track for optimal read performance. To that end, the servo controller 20 is configured to support a read servo process 34 that receives as an input read data signal quality values (produced by the data channel unit 30) and provides as an output to the head stepper 32 position adjustment control signals for fine-tuning the position of the data head structure relative to a selected track from which data is being read, as will be described. Thus, the servo controller 20 provides two levels of positioning, a coarse level for track switching and a finer level for maintaining track centerline.

It will be understood that the term "data" as used herein relates to user data, that is, the information provided to the system 10 via the host interface 24, as opposed to servo information (information that is produced and stored on a tape in order to provide corrective positioning information). The tape drive system 10 does not employ servo heads or servo information.

Referring to FIG. 2, the PRML data channel unit 30 includes a write path 40 and a read path 42. The write path 40, which is coupled to the data controller 26 and the preamp and write driver block 28, includes an encoder 44, a pre-coder 46 and a write precompensation circuit 48. The read path 42, also coupled to the data controller 26, the preamp and write driver block 28, as well as the servo controller 20, includes an analog signal processing block 50, an analog-to-digital converter ("ADC") 52, a finite impulse response ("FIR") filter 54, a Viterbi detector and decoder 56, and a Quality Monitor ("QM") unit 58. The QM unit 58 includes an error value computation



block 60 for computing signal quality (or error) values for read data signals and an accumulator register 62 for summing the values produced by the error value computation block 60 to produce read data signal quality values. The read servo process  
5 34 (shown in FIG. 1) samples the contents of the accumulator register, as will be described in greater detail below with reference to FIGS. 3 through 7.

The data channel unit 30 can also include other components, such as digital timing control circuits for providing  
10 timing adjustment control to the VCO, a digital gain control circuit for applying a gain control via a gain control DAC to the VGA, and a digital offset control circuit for applying a DC offset control value via an offset DAC to the offset correction amplifier.

15 Still referring to FIG. 2, the process of storing and retrieving data within the tape drive system 10 using the data channel unit 30 will be described. Data to be written is applied to an encoder 44 by the data controller 26 (from FIG. 1). The encoder 44 produces a modulation coded output having predefined  
20 run length constraints, such as for the minimum and maximum number of consecutive zeros and the maximum run length of zeros in the even and odd recorded sequences in the overall recorded sequence. The output of the encoder 44 is provided to the pre-coder 46, which is used to cancel the effect of the equalized  
25 data head transfer function, and thus permits the data written to encoder 44 to be the same as the data output from the decoder 56. Thus, the encoder 44 and the pre-coder 46 encode and precode write data into a coded data pattern having a desired partial response spectrum (e.g., PR4) suitable for writing on the tape 16  
30 in a pattern manifesting controlled intersymbol interference. The write precompensation circuit 48 is coupled to the pre-coder 46 and provides a modulated binary pulse signal to a write driver

of the block 28 (of FIG. 1).

An analog read data signal obtained at the data head is described by a transfer function. The read data signal is processed by the analog signal processing block 50. The analog  
5 signal processing block 50 typically includes a variable gain analog amplifier (VGA), a programmable analog filter/equalizer and a DC offset correction amplifier, as is known in the art. The output of the block 50 is converted to digital form by the ADC 52. Digital filter 54 receives and filters the sampled  
10 values of the ADC 52, and produces as an output equalized data samples. The output of digital filter 54 is applied to a Viterbi detector and decoder 56.

The output of the digital filter 54 is also applied to the QM unit 58. The error value computation block 60 takes as  
15 its input the equalized data samples and produces error value samples generated from the equalized data samples. The block 60 generates an error value sample by calculating the difference between an ideal (or expected) value of the data sample and the observed value of the data sample. The block 60 squares the  
20 error value samples (the squared error values also referred to as the Root Mean Squared or "RMS" error). The RMS error is a useful predictor of channel Bit Error Rate, and can be measured quickly. The greater the error values, the higher the noise levels. The block 60 provides the RMS error value sample to the accumulator  
25 register 62, which sums the squares of each error value sample. The contents of the accumulator register 62 are thus indicative of read data signal quality. The length of time that the error value samples for a read event are provided to the accumulator register is programmable. For example, it can be selected for a  
30 number of bytes. In the described embodiment, the period is defined by a window of 32 bytes.

The read servo process 34 (from FIG. 1) samples the



to a "stepping" state, the process 34 performs the stepping state operation (step 86). If the "state" is a "streaming\_no\_step" state, the process 34 performs the operation for that state (step 88). The steps (and corresponding state operations) 82, 84, 86 and 88 will be described more fully later with reference to FIGS. 4, 5, 6 and 7, respectively. Generally, that portion of the process 34 that encompasses the "initial\_no\_step", "initial\_step" and "stepping" states may be viewed as a seek mode of operation. Between seek modes, when the process 34 is in the "streaming\_no\_step" state, the process 34 continues error value sampling without stepping activity, as will be described. The data head continues to read from the tape and produce error values during the read servo processing.

Still referring to FIG. 3A, since the variable "state" is equal to "initial\_no\_step" state, the process 34 proceeds to the "initial\_no\_step" state 82. Upon completion of that state's (or any other state's) operation, the process 34 determines if the value of a variable "stay\_in\_loop", which is set during the previous state operation, is "TRUE" (step 90). If it is, the process 34 returns to the state determination 80. If it is not, the process 34 increments by one the variable "step\_index" (step 92). The process 34 determines if the value of "step\_index" is greater than a maximum step index threshold, in this example, a value of 8 (step 94). If the value of "step\_index" is greater than 8, the process 34 resets that variable to zero and a "sign" variable equal to one (step 96), and returns to step 74.

Otherwise, and now referring to FIG. 3B, the process 34 determines if a variable "step\_size" is greater than zero (step 98). The value of "step\_size" corresponds to the number of steps to be performed between error value samples. A "step" unit is 1/12 of a milli-inch; however, other values can be used. If the process 34 determines the value of "step\_size" to be greater than



retrial (as indicated by the vertical retry offset). The process 34 proceeds to set "last\_sign" equal to "sign", "step\_size" equal to '1' and "state" equal to the "initial\_step" state (step 126). The process also sets the value of "stay\_in\_loop" equal to

5 'FALSE' (step 130) and returns to step 90 (FIG. 3A).

Referring back to FIGS. 3A/B, if the "step\_index" value is less than 8, and the step\_size is greater than zero (step 98), the process 34 causes the head stepper to step the head a number of steps equal to (sign x step\_size) and samples the error value  
10 for the read data signal at the current location and stores the sampled error value in the "sum" variable. The process then proceeds to the "initial\_step" state.

Referring to FIG. 5, the process 34 begins the "initial\_step" state by setting "initial\_sum\_QM\_step" equal to  
15 the value of "sum" (step 130). The process 34 determines if (initial\_sum\_QM\_step - initial\_sum\_QM\_no\_step) is less than or equal to zero (step 132). That is, the process 34 determines if the amount of error value has decreased as a result of the stepping, as indicated by the sign of the difference. If so, the  
20 process 34 sets "step\_size" to '1' and the variable "last\_sum\_QM" to the value of "sum" (step 134). Otherwise, if the change in values indicates that the noise level has increased, the process 34 changes the value of "sign" to the value of "sign" multiplied by '-1', sets "step\_size" equal to '2' and set "last\_sum\_QM"  
25 equal to 'initial\_sum\_QM\_no\_step' (step 136). By changing the sign and changing the "step\_size" value to '2', the process changes the direction of the stepping and sets up the stepper to move back one step and then over another step to see if there is improvement when moving in the opposite direction. The process  
30 34 updates the state variable to the "stepping" state (step 138), sets the variable "stay\_in\_loop" equal to 'FALSE' (step 140) and returns to step 90 (FIG. 3A) to perform the processing steps of

FIG. 3A and 3B as discussed above.

Referring to FIG. 6, the process 34 begins the stepping state by determining if the most recent reduction in the error value, i.e., the absolute value of the difference between the values of "sum" and "last\_sum\_QM", is below a lower threshold equal to hysteresis and the value of

"number\_of\_cycles\_for\_initseek" is greater than a "minseeksteps" threshold value corresponding to a threshold number of allowable steps (step 150). If yes, the process 34 sets "step\_size" equal to zero and "state" equal to "streaming\_no\_step" (step 152).

Thus, if the conditions of step 150 are both satisfied, the process terminates the seek mode, and the current position of the data head is used as a baseline for track center. If the conditions are not satisfied, the process 34 continues with the

stepping activity. It increments by one the value of "number\_of\_cycles\_for\_initseek" (step 154) and determines if the value of "sum" minus the value of "last\_sum\_QM" is less than zero (step 156). If it is, the process 34 sets "step\_size" equal to '1' (step 158). Otherwise, it sets the value of "step\_size" equal to '2' and changes the value of "sign" to the value of "sign" multiplied by '-1' (step 160). After completing any one of steps 152, 158 and 160, the process 34 then updates "last\_sum\_QM" with the value of "sum" (step 162) and sets "stay\_in\_loop" to a 'FALSE' value (step 164). The process returns to step 90 (FIG. 3A).

Referring to FIG. 7, the process 34 commences the "streaming\_no\_step" state by incrementing the value of "number\_of\_cycles" (step 170). The process 34 determines if that value is now greater than or equal to a maximum parameter value "reseek\_count", or if the absolute value of the difference between the values of "sum" and "last\_sum\_QM" is greater than hysteresis (step 172). If the process determines that either of

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those conditions is true, the process 34 sets the state to  
"initial\_no\_step" (step 174), sets the value of "stay\_in\_loop" to  
'TRUE' (step 176) and returns to step 90 (FIG. 3A). That is, the  
process returns to the seek mode. Otherwise, the process 34  
5 continues cycling through the streaming state until that result  
changes. The process 34 sets the value of "step\_size" equal to  
zero and the value of "last\_sum\_QM" equal to the smaller of the  
"sum" and "last\_sum\_QM" values (step 178), sets the variable  
"stay\_in\_loop" equal to a 'FALSE' value (step 180) and returns to  
10 step 90 (FIG. 3A).

Thus, in seek mode, the process 34 samples a baseline  
signal quality level at a current tracking position (by reading  
an initial error value from the accumulator register) and begins  
an iterative process of stepping the data head in small amounts,  
15 sampling the register and determining from the sampling a change  
in error value in order to guide the stepping activity towards  
error value reduction. Once a satisfactory degree of signal  
quality improvement is achieved, for example, and as described  
above, the change in error value and the number of steps reach  
20 predefined thresholds (and therefore little improvement is to be  
gained by further stepping activity), the process 34 stops  
stepping and uses the new error value as a new baseline signal  
quality level. During the streaming state, the error value is  
again sampled and, if the error value has changed significantly,  
25 the process 34 returns to the initial state ("initial\_no\_step")  
of the seek mode and starts stepping the data head to search for  
a new baseline level. As described above, the streaming state  
also allows for a return to the seek mode operation if the amount  
of time that has expired since the last seek mode operation ended  
30 is greater than some larger interval (e.g., 30 seconds). The  
process 34 is able to start seeking at the initial state,  
searching for the new track center on the current section of tape



that may provide a more optimal baseline.

Other embodiments are within the scope of the following claims.

What is claimed is:

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